

ABSTRACT OF THE DISCLOSURE

A timing information generating apparatus includes an input/output information identifying unit, a delay time calculating unit and a timing information output unit. The

5 input/output information identifying unit compares logical connection information with a library to identify intra-block input stage sequential circuits contributing to information exchange with extra-block input stage sequential circuits, and intra-block output stage sequential circuits contributing to

10 information exchange with extra-block output stage sequential circuits. According to timing constraint information, the delay time calculating unit sets first delay times from input pins to the intra-block input stage sequential circuits, and second delay times from the intra-block output stage sequential circuits

15 to output pins. The timing information output unit outputs timing information including the first delay times and the second delay times.